



Optimize Your Test Performance

FINLEY DESIGN SERVICES, INC.

910 Bern Court , Ste 130
San Jose, Ca 95112

Tel: 408-437-4080

Email: sales@finleydesign.com

Web: www.finleydesign.com

Company Profile

- ❑ Leading supplier of Load Boards, Probe Cards & D.U.T Boards for the I.C. Test Industry
- ❑ Support for all Testers, Handlers & Sockets
- ❑ Highly experienced staff in the Back End Test & Assembly market
- ❑ Quality fabrication and assembly with ongoing support
- ❑ Financially stable in current market conditions

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Founders

Founders:

- Melodye Finley, Sales & Marketing Manager
 - 30+ years of experience in Load Boards, Probe Cards & D.U.T Boards
- Gerry Finley, Design Manager
 - 20+ years of experience in the design and development of Load Boards, Probe Cards and D.U.T. Boards

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Management Philosophy

- “EMS” – Excellent Manufacturing Support
- Continuous Process Improvement
 - Project Review and Strategy
 - Flow – Scheduling – Complexity
 - TQM: Total Quality Management
 - People – Equipment – Methods – Materials
 - Product Enhancement
 - DRC: Design Rule Check
 - DFM: Design for Manufacturability
 - New Product Review
 - JIT: Just in Time
 - Strong Supplier Partnerships
 - First in, First out

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Competitive Advantages

- Totally Engineered Solutions
- Design, Fabrication & Assembly
- High Speed, Low Noise
- Propagation Delay Matching
- Controlled Impedance
- Shielded Traces
- High Layer Counts
- Blind & Buried Vias
- Conductive Vias
- Exotic board materials
- Ongoing Support

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Design Tools

Layout

- Cadence Allegro PCB
- AutoDesk AutoCAD – Enhanced

Schematic Capture

- ORCAD
- Cadence Concept

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Typical Design Cycle

- Receive Customer Input
- Verify Completeness
- CAD Design Layout
- Provide Check Plots, Net List, Schematics to customer
- Receive Customer Approval
- Develop CAD files for PCB Fabrication
- Fabricate Printed Circuit Boards
- Incoming QA/QC
- Board Assembly and Delivery

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Front End Engineering

- External circuitry, analysis and component selection assistance
- Failure analysis of chip/board/test program interaction problems
- Impedance Modeling
 - Single, Differential, Coupled Impedance
- Mechanical evaluation and guidance
- Development of Design Specification

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Technical Capabilities

- Laminates
 - FR-4 Epoxy, Rogers Polyimide, Teflon, Nelco
- Holes
 - Aspect Ratio of >15:1
 - NPT Hole Tolerance +/- 0.001
 - Hole to Hole Location Accuracy +/- 0.001
- Maximum Thickness: .280"
- Matched Impedance
 - +/- 5%
- Minimum Dielectric Space-Cores: .003-.004"

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Technology Sectors

Products

- Digital
 - High Speed, LVDS
- Mixed Signal
 - Power, LAN, A-D, Graphics
- Memory
 - DDR, Rambus
- RF
 - Up to 16 GHz

Package Types

- PLCC
- TQFP
- SOIC
- TSSOP
- CSP
- QFN
- BGA
 - 1.00mm, .8mm, .5mm, .4mm, .35mm

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Design Process

- Schematic Capture
 - Creation
 - Editing
 - Delay Statistics
- CAD Design Layout
 - Placement
 - Routing
- Net List Extraction
 - From Design Data
 - Comparisons
 - Propagation
- Check Plots
 - Soft Copy
 - Net List
 - Bill Of Materials
- Design Verification
 - Gerber Extraction
 - Net List Extraction
 - DRC for DFM

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QA / QC Process

- Incoming Inspection
 - Opens / Shorts
 - Net List
 - Leakage
 - Mechanical
 - Thickness
 - Hole sizes
 - Plating
- Outgoing Inspection
 - Component Placement
 - Opens / Shorts
 - Leakage

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Product Delivery

- Final Documentation (CD containing the following)
 - BRD Files for project
 - PDF's of all layers
 - Net List with lengths
 - Copy of Schematic
 - Bill Of Materials
 - T.D.R. Plots
- Shipments
 - Local Vendor Delivery
 - Worldwide Delivery via FedEx

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by quality people

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Contact Us Today!



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